

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of: Shen et al.

Application No.: 10/582,035

Confirmation No.: 1665

Filed: May 7, 2007

Art Unit: 4146

For: Monolithic Power Semiconductor Structures

Examiner: R. T. Huber

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (SIDS)**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08 form. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

In accordance with 37 CFR 1.98(a)(2)(ii), Applicants have not submitted copies of cited U.S. patents and U.S. patent applications. Applicants submit herewith copies of non-patent literature in accordance with 37 CFR 1.98(a)(2). This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action after the filing of a Request for Continued Examination under 37 CFR 1.114 (37 CFR 1.97(b)(4)).

No fees are believed necessary for filing this Supplemental Information Disclosure Statement. However, if any fees are due, the Director is hereby authorized to charge such fees to our Deposit Account No. 07-1700, under Order No. GWS-009.

Dated: December 12, 2008

Respectfully submitted,

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